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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT T. TANIMOTO, et al	
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U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA						
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AF						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Abstract	
						Yes	No
AG							
AH							
AI							
AJ							
AK							
AL							
AM							
AN							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	
AP	
AQ	
AR	H. OKEWATARI, et al., "Multi Processor ni yoru Bunsan Shori o Ishiki Shita Senyo Processor Sekkei Shien System SYARDS no Kochiku", Information Processing Society of Japan Kenkyu Hokoku, Information Processing Society of Japan, 20 January, 1995, Vol. 95, No. 6 (DA-73), pages 105 - 112.
/P.S./ AS	S. KITAGUCHI, et al., "Jitsujikan Seiyaku o Yusuru Tan'itsu Bus System no JAVA ni yoru Model-ka yoobi Parametric Model Checking o Mochita Sekkei Shubo no Teian", Information Processing Society of Japan Kenkyu Hokoku, Information Processing Society of Japan, 28 Nov. 2002, Vol. 2002, No. 113 (SLDM-107), pages 19 to 24.
AT	
/P.S./ AU	"Design and Implementation of Priority Queuing Mechanism On FPGA Using Concurrent Periodic EFSMs And Parametric Model Checking" by T. KITANI, et al.
AV	
AW	
AX	
AY	
AZ	
Examiner	Date Considered
/Patrick Sandoval/	08/15/2008

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /P.S./